

FIG. 1A

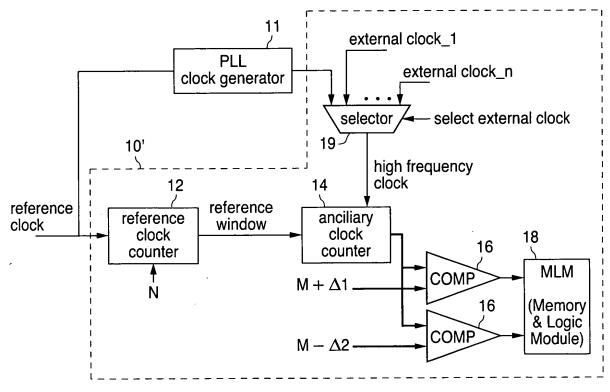
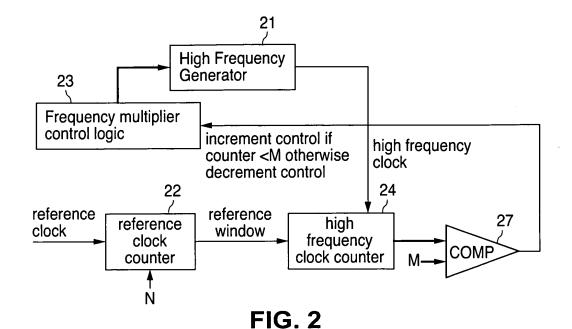


FIG. 1B



21 High Frequency Generator 23 Frequency multiplier control logic increment control if counter <M otherwise high frequency decrement control clock reference reference high 27 reference clock window frequency clock COMP clock counter counter 10" 24 **↑** N 22 COMP $M + \Delta 1$. MLM 26 COMP 20-28 -

FIG. 3